IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Peyman Hadizad

FILED:

Concurrently Herewith

FOR:

DC/DC CONVERTER WITH DEPLETION MODE

COMPOUND SEMICONDUCTOR FIELD EFFECT

TRANSISTOR SWITCHING DEVICE

Date:

7/18/2003

DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.56

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted,

Peyman Hadizad

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F rm PTO-1449	ATTY. DOCKET NO.	SERIAL NO.	
LIST OF PATENTS AND PUBLICATIONS	Ons00502		
FOR APPLICANT'S INFORMATION	APPLICANT Peyman Hadizad		
DISCLOSURE	FILING DATE	GROUP	
STATEMENT (Use Several Sheets if Necessary)	CONCURRENTLY		

REFERENCE DESIGNATION U.S. PA									PAT	TENT DOCUMENTS				
Examin Initial	ier	Docum	nent	Nu	mbe	r				Date	Name	Class	Sub- Class	Filing date (If approp rirate)
	AA	6	3	5	6	0	5	9		3/12/2002	Yu	G05F	1/613	
	AB	5	6	1	0	0	8	5		3/11/1997	Yuan et al	H01L	21/265	
	AC	5	2	3	1	0	3	7		7/27/1993	Yuan et al	H01L	21/44	
	AD	5	2	6	2	2	9	6		4/14/1981	Shealy et al	H01L	29/06	
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FOREIGN PATENT DOCUMENTS

Examiner Initial		Document #				Date	Country	Class/s	Translation Yes - N							
	AN	E P	0	8	7	4	3	9	4	10/28/19 98	EPO		H01L	21/337		
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	AR															

		OTHER ART (Including Aut)	hor, Title, Date, Pertinent Pages, Etc.)
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	АТ		
	AU		
	AV		
	AW		
EXAMINER			DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.